



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Here Application of :

Robert Madge
Kevin Cota
Bruce Whitefield

Serial No. : 10/020,407

Filed : December 12, 2001

For : METHOD OF DETECTING
SPATIALLY CORRELATED
VARIATIONS IN A
PARAMETER OF AN
INTEGRATED CIRCUIT DIE

Group Art Unit : 2822

Examiner : Trinh, Michael M.

Atty Docket : 71739 (6653) / 01-299

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Castillo

5/18/04 *Connie del Castillo*
Date Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Date: 5/18/04

Respectfully submitted,

Leo Peters

Reg. No. 33,562